

ABSTRACT

A multi-chip module comprising a low-temperature co-fired ceramic substrate having a first side on which are mounted active components and a second
5 side on which are mounted passive components, wherein this segregation of components allows for hermetically sealing the active components with a cover while leaving accessible the passive components, and wherein the passive components are secured using a reflow soldering technique and are removable and replaceable so as to make the multi-chip module substantially programmable with regard to the
10 passive components.